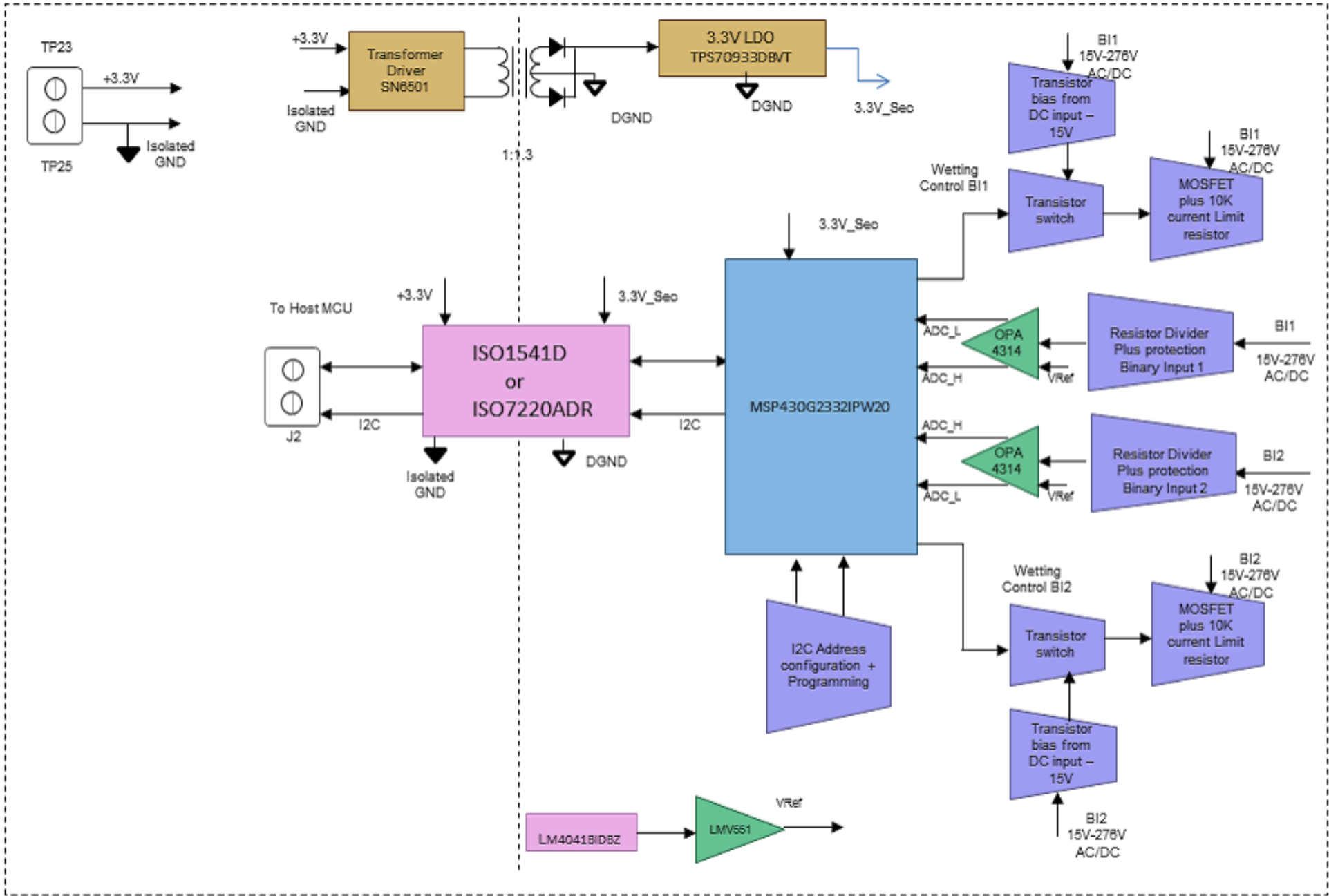
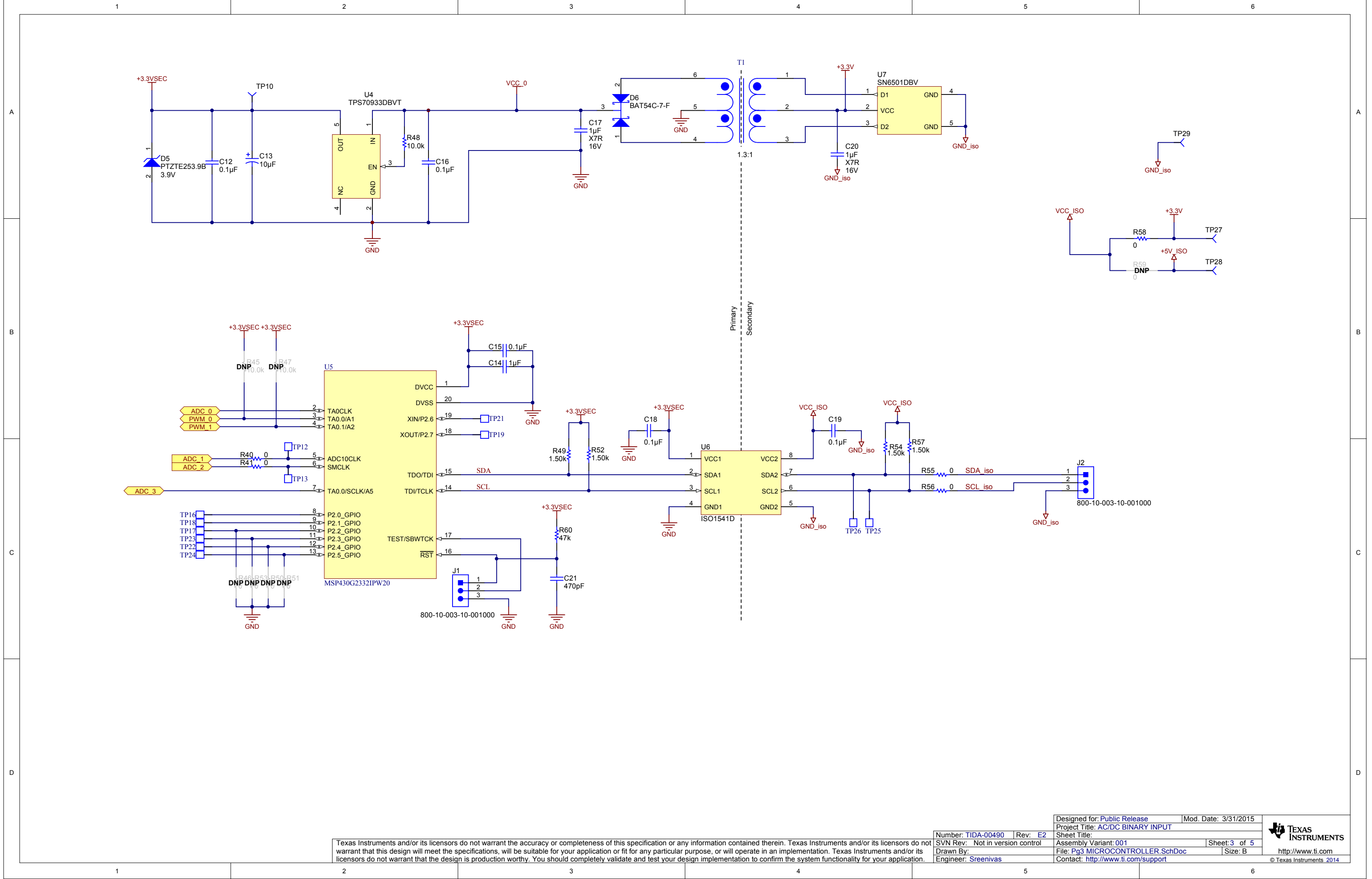


1	2	3	4	5	6						
A					<table><tr><th colspan="2">Revision History</th></tr><tr><th>Revision</th><th>Notes</th></tr><tr><td></td><td></td></tr></table>	Revision History		Revision	Notes		
	Revision History										
	Revision	Notes									
<div><div>Page 2</div><div>BLOCK DIAGRAM</div></div>											
B	<div><div>Page 3</div><div>MICROCONTROLLER</div></div>										
	<div><div>Page 4</div><div>BINARY INPUT</div></div>										
C	<div><div>Page 5</div><div>HARDWARE D25-IO_ANSI-B</div></div>										
D											
				<div><div></div><div>Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.</div></div>							
1	2	3	4	5	6						

AC/DC binary input





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